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MS-7174H1

Version 1B

Intel (R) Grantsdale (GMCH) + ICH6 Chipset
Intel Prescott LGA775 Processor

CPU:

Intel Prescott Celeron D 350J (3.2GHz), P4 550J (3.4GHz)

System Chipset:

**Intel Grantsdale - GMCH (North Bridge 915GV or 910GE)
Intel ICH6 (South Bridge)**

On Board Chipset:

**BIOS -- FWH FLASH 4Mb
AC97 AUDIO -- ALC880
LPC Super I/O -- SMSC47M997
LAN -- Intel 82562GT
1394 -- VIA VT-6307
CLOCK -- Cypress CY28416**

Main Memory:

2 CHANNEL DDR II * 1 (Max 2GB)

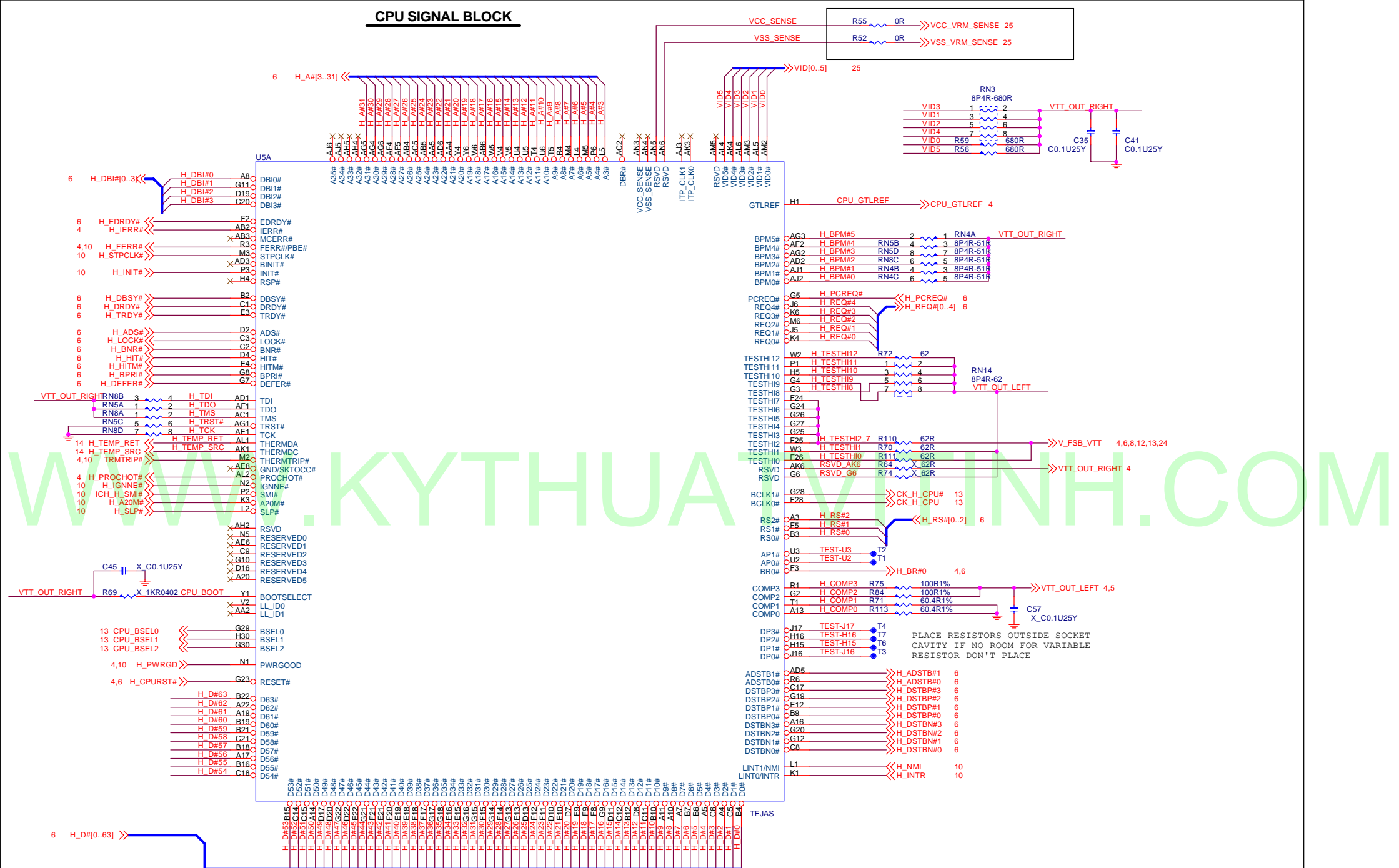
Expansion Slots:

PCI 2.3 SLOT * 3

Intersil PWM:

Controller: INTERSIL 6566 3PHASE

CPU SIGNAL BLOCK




3,4 VTT_OUT_LEFT

R73 X 60.4R1%
R76 X 60.4R1%

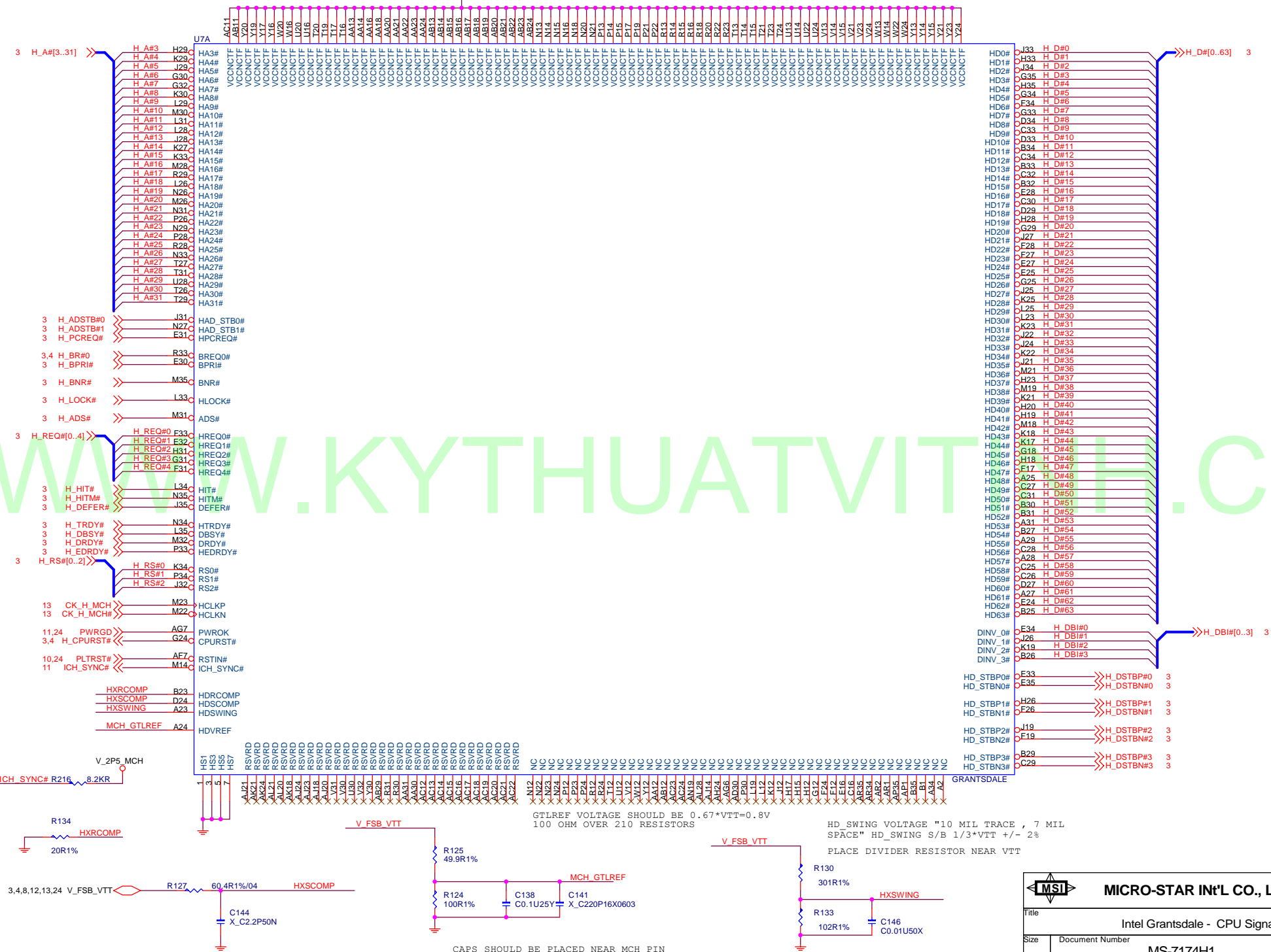
USC



TEJAS

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Title Intel LGA775 CPU - GND		
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V_1P5_CORE



GTLREF VOLTAGE SHOULD BE $0.67 \cdot V_{TT} = 0.8V$
100 OHM OVER 210 RESISTORS

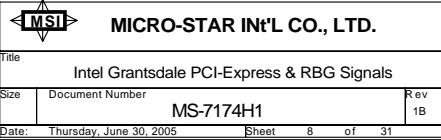
HD SWING VOLTAGE "10 MIL TRACE , 7 MIL
SPACE" HD_SWING S/B $1/3 \cdot V_{TT} \pm 2\%$

PLACE DIVIDER RESISTOR NEAR VTT

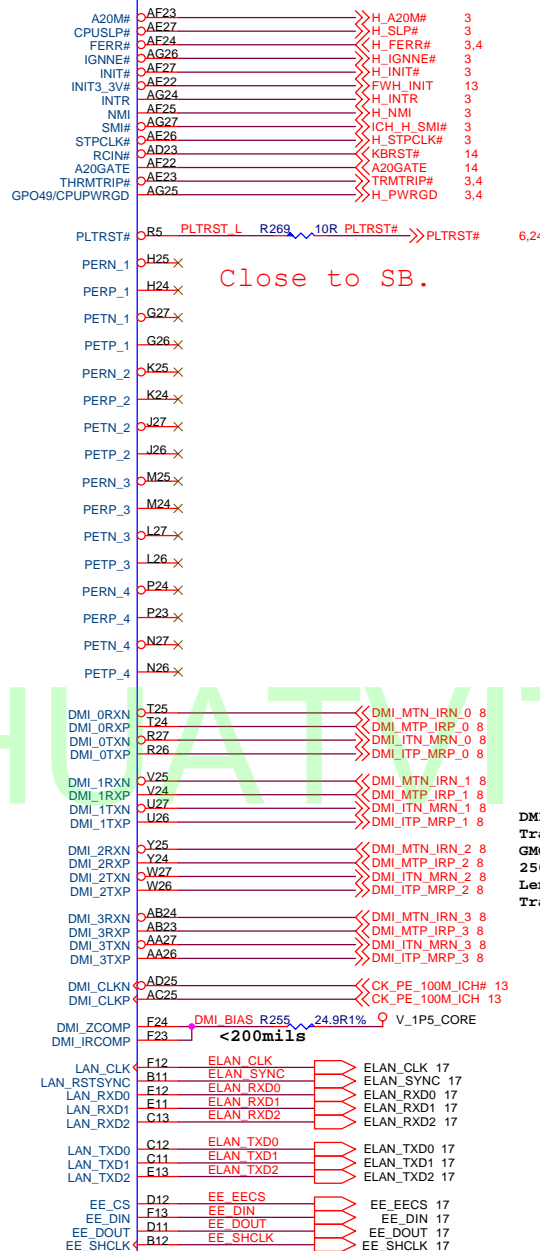
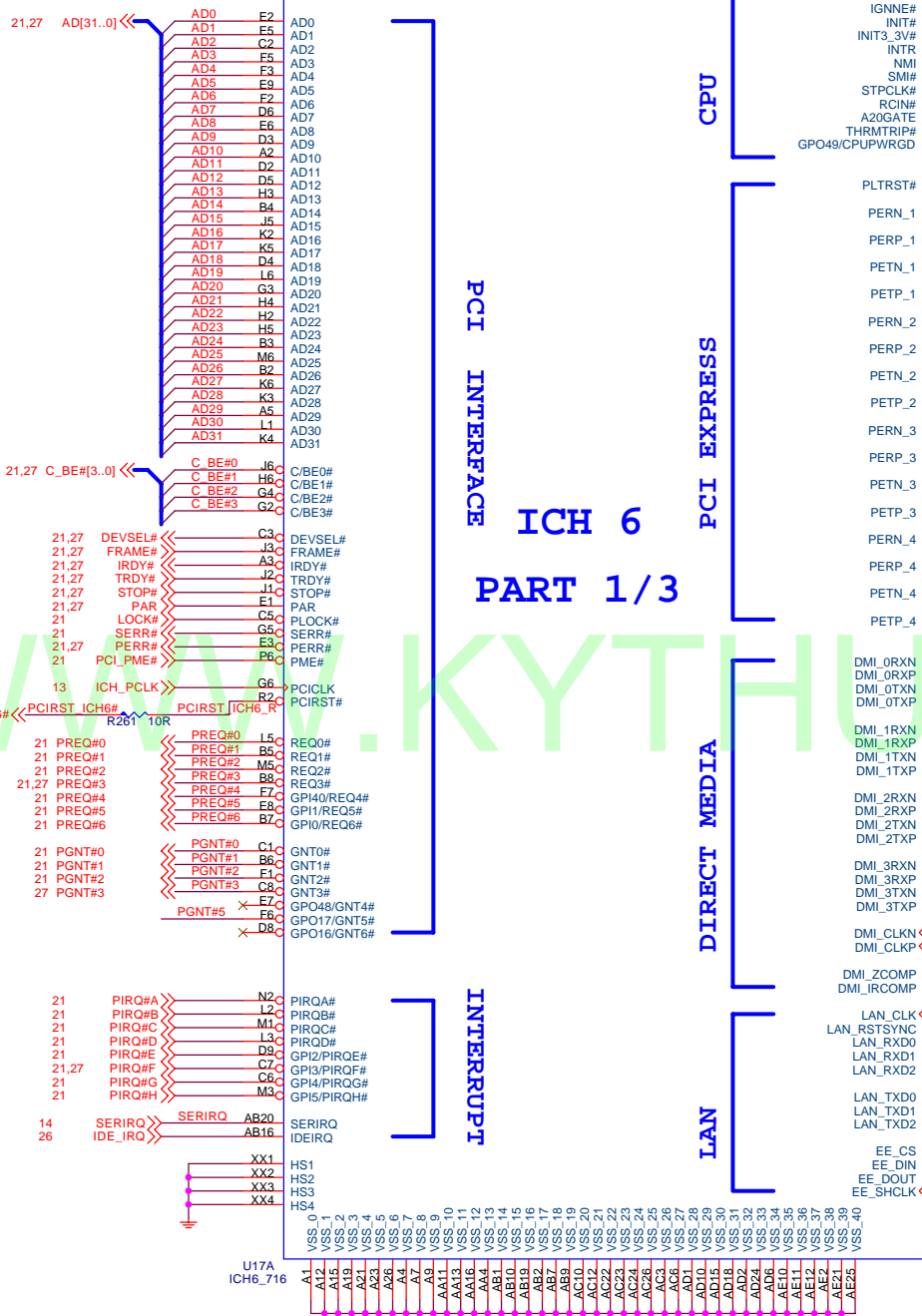


MICRO-STAR INT'L CO., LTD.

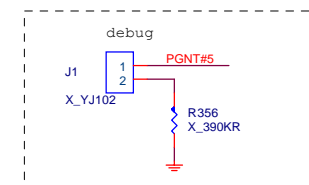
Title			Intel Grantsdale - CPU Signals
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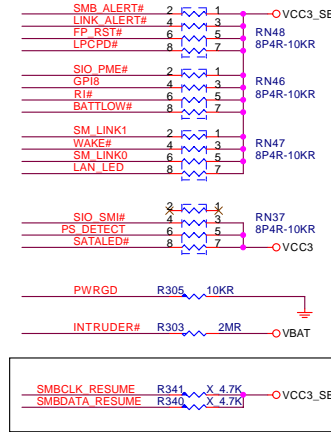
DMI Interface
Trace width 5 mils & 7 mils space.
GMCH breakout space 5 mils, length < 250 mils
Length matching < 5 mils
Trace Length 2" to 11"



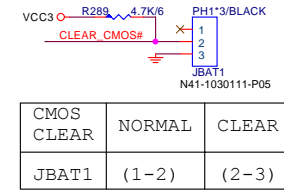
ICH 6 PART 2/3

ICH6 STRAPPING RESISTORS

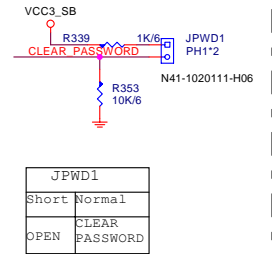
ALL COMPONENTS CLOSE TO ICH6
Trace length is less than 3inches to ICH6.



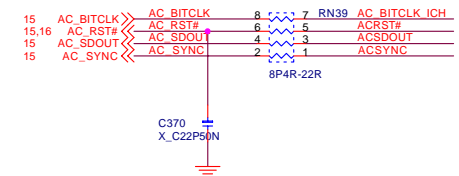
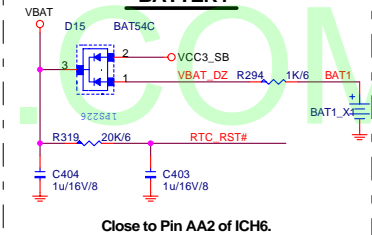
CLEAR CMOS JUMPER



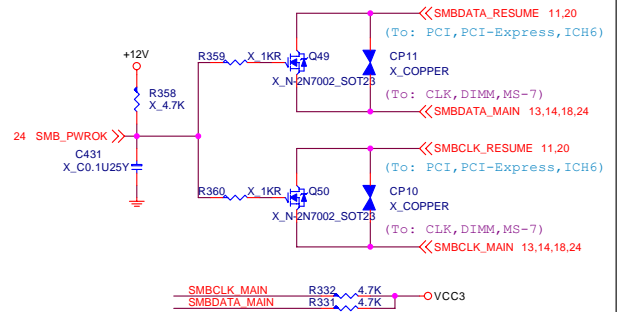
PASSWORD CLEAR JUMPER



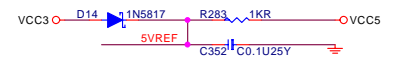
BATTERY



SM BUS ISOLATION



5VREF Sequencing Circuit



ICH 6 PART 3/3

1.5V DMI POWER

1.5V CORE WELL POWER

S0 POWER

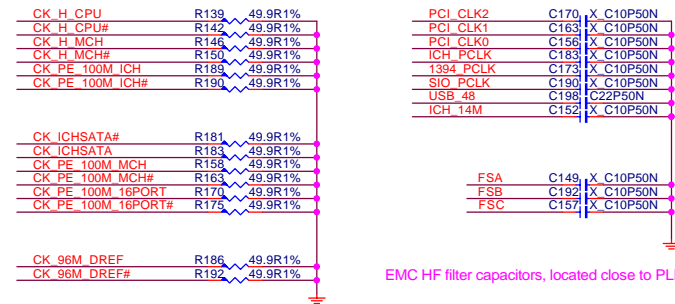
MISC

S5 POWER

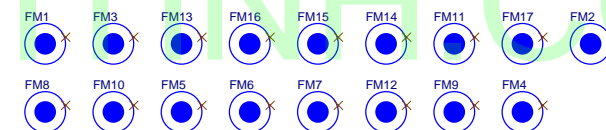
U17C
ICH6_716

MICRO-STAR INT'L CO., LTD.		
Title	Intel ICH6 - POWER	
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Trace length less than 0.5inches

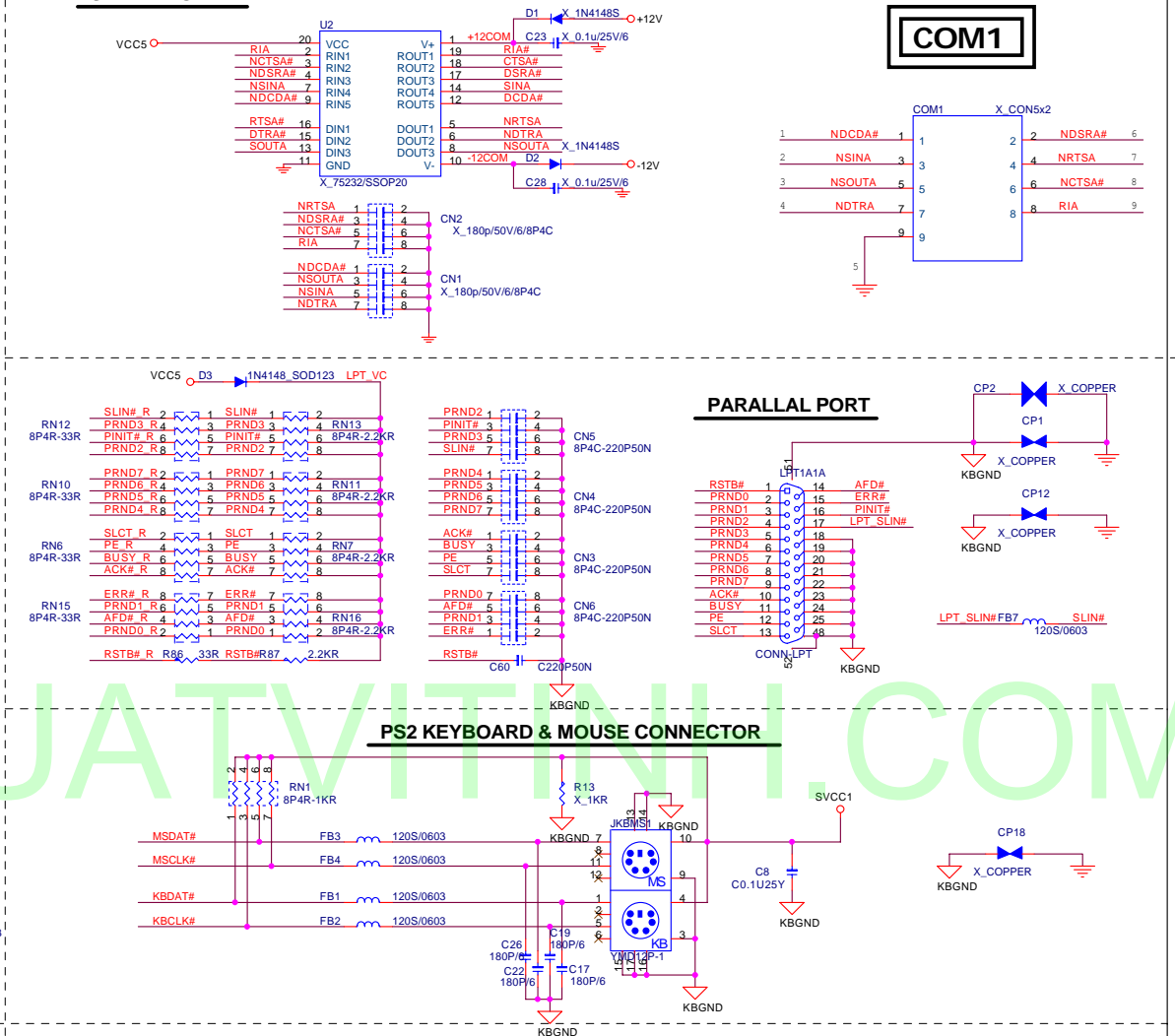
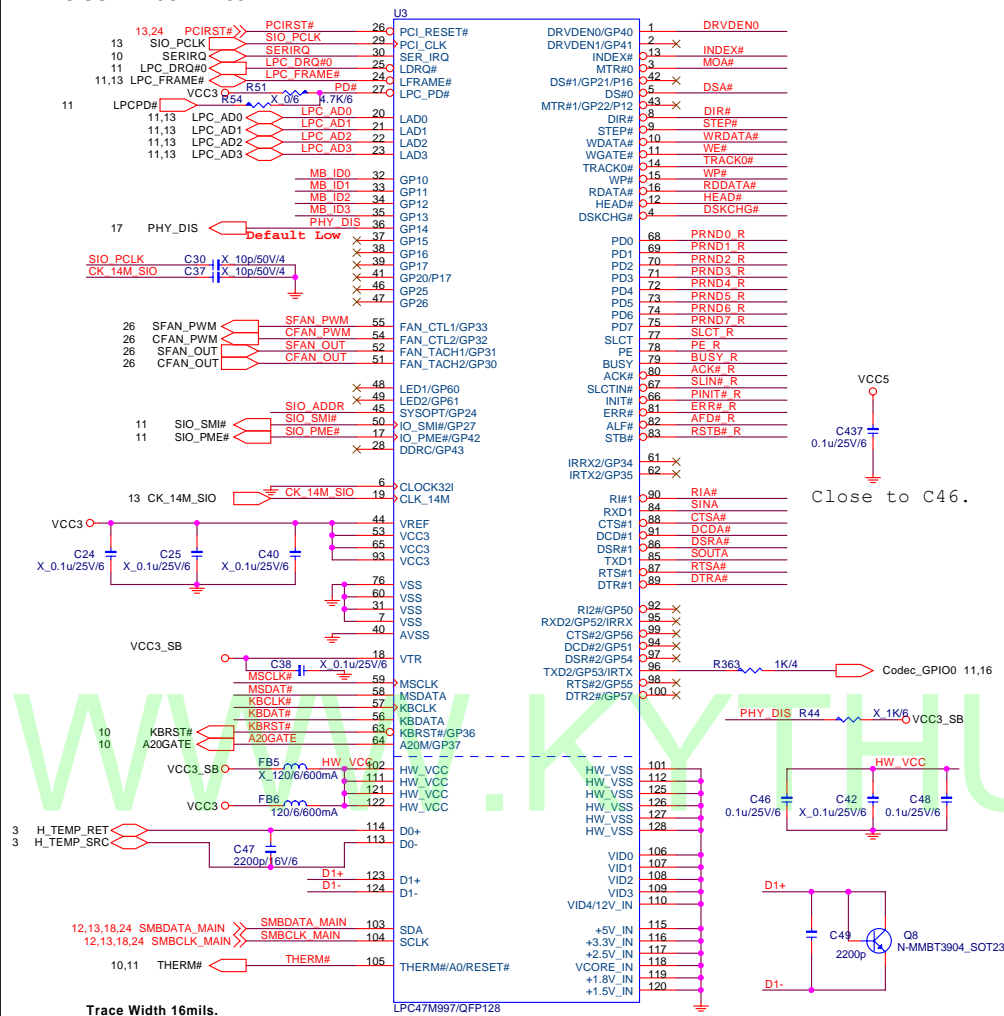
[illegible]

If you place the jumper very closed to FWH bios socket, please use the same clock with FWH. But if you can not place it so close, please use another clock to support it.

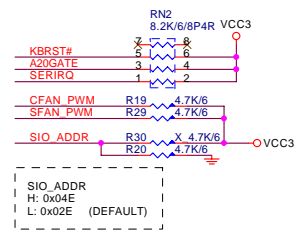


default is high

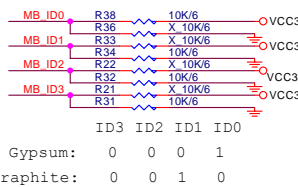
RES1 1 2 VCC3
FWH WP#3 3 4
RES2 5 6
RES3 7 8
RES4 R120 1KR
FWH INTR153 X 8.2KR VCC3



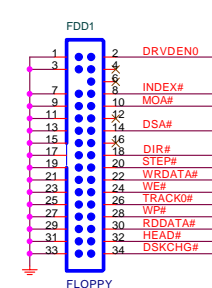
STRAPPING RESISTOR



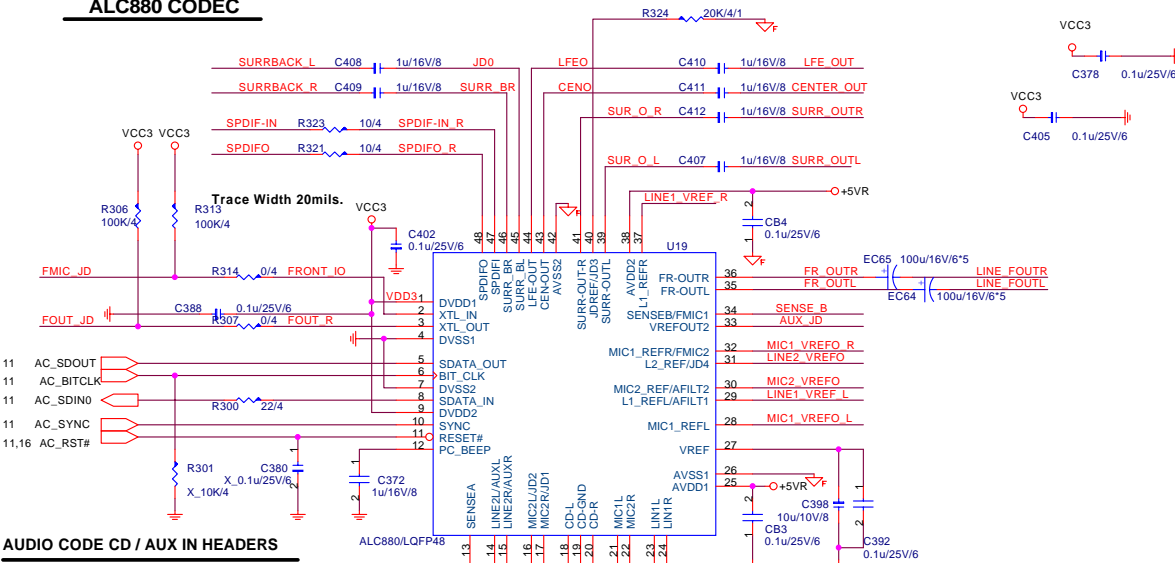
M/B Revision ID



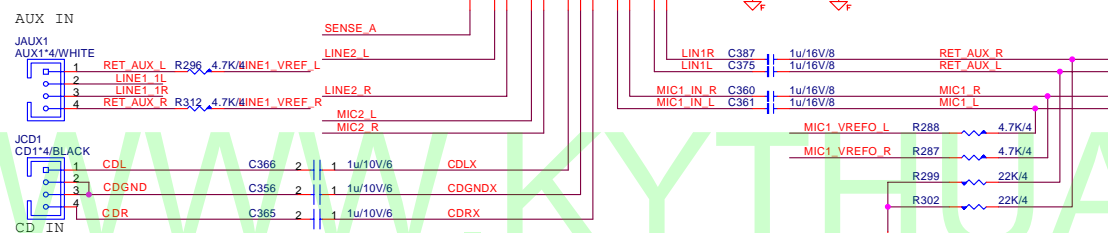
FLOPPY CONNECTOR



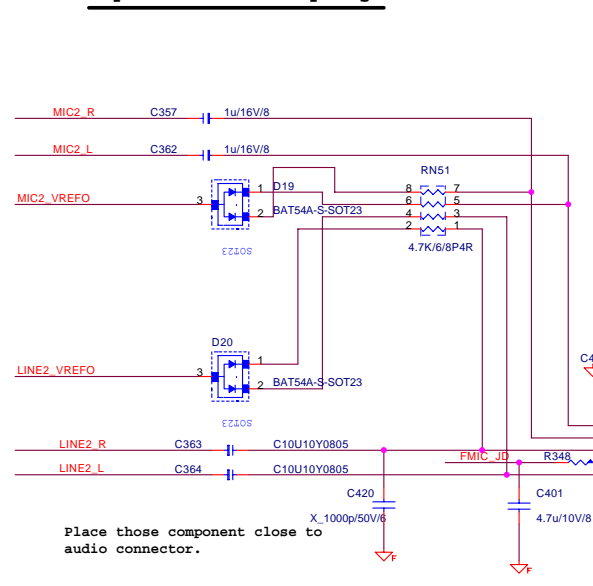
ALC880 CODEC



AUDIO CODE CD / AUX IN HEADERS

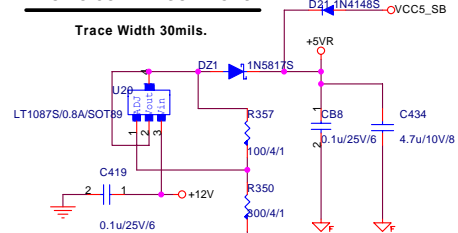


Speaker Out Decoupling

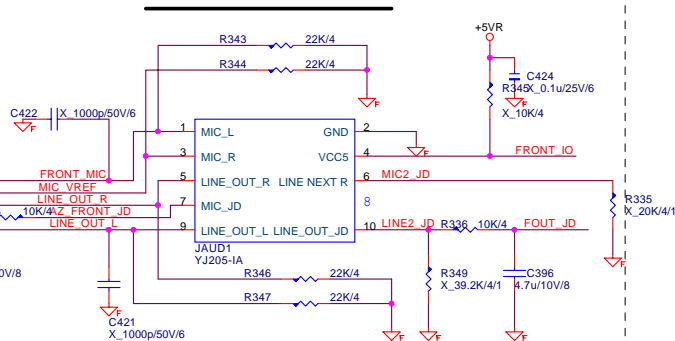


Place those component close to
audio connector.

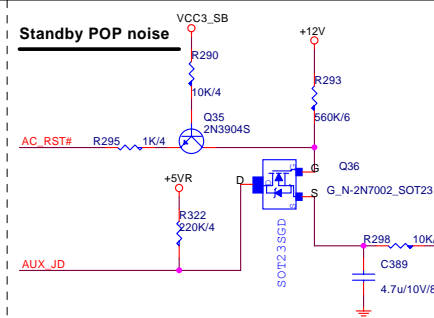
AUDIO CODE REGULATORS



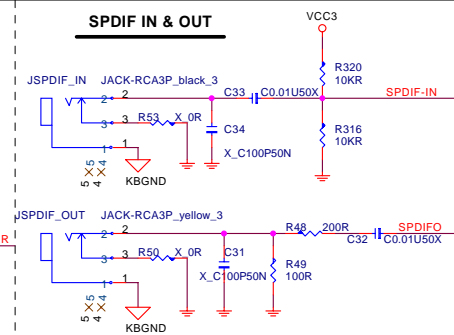
Azalia Front Audio Connector



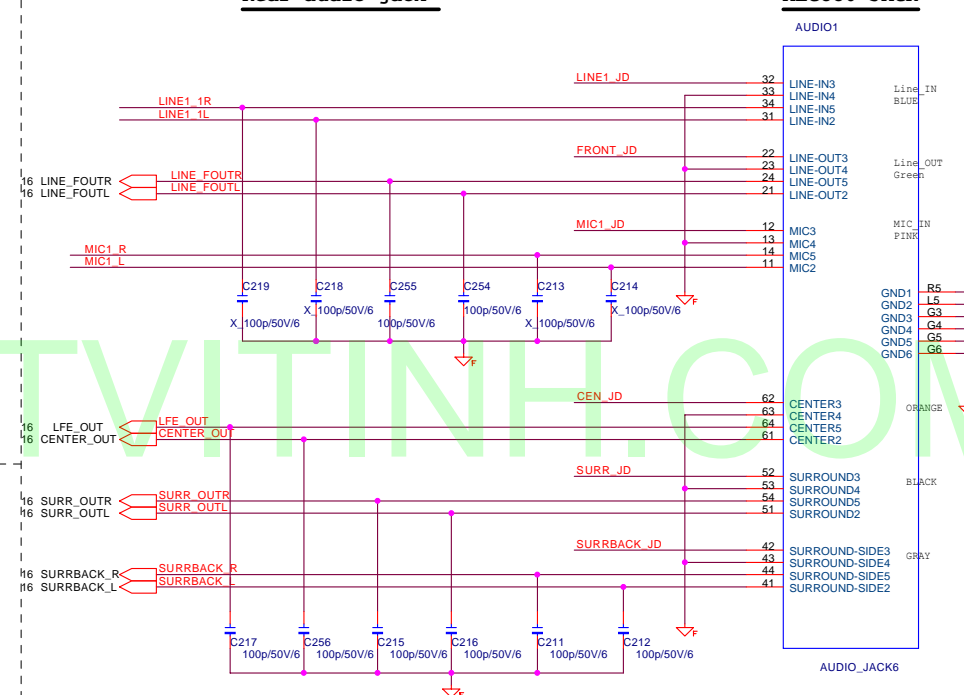
Standby POP noise



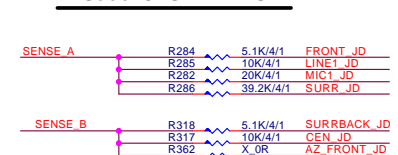
SPDIF IN & OUT



Rear audio jack



ALC880 JACK DETECT

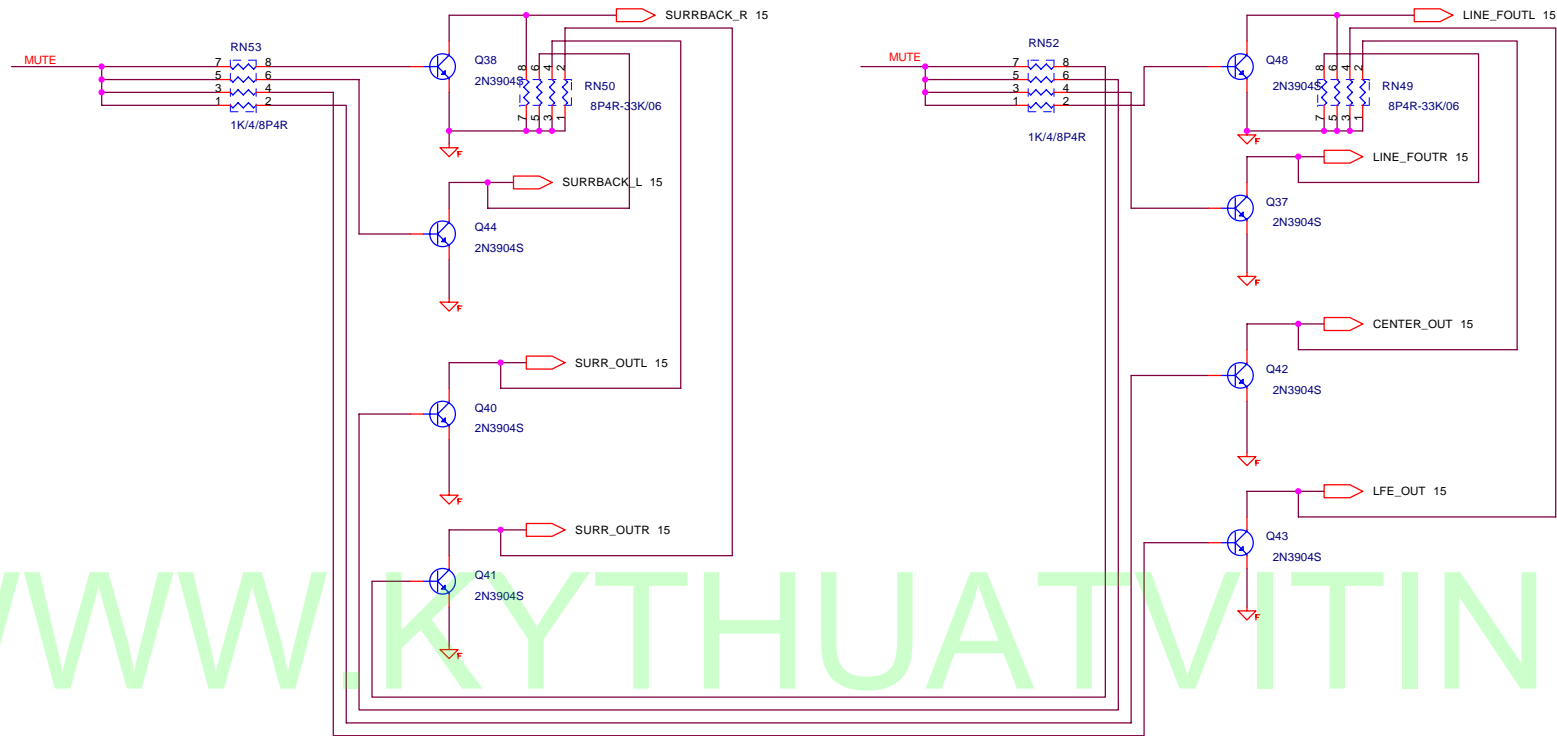


Closer to Codec.

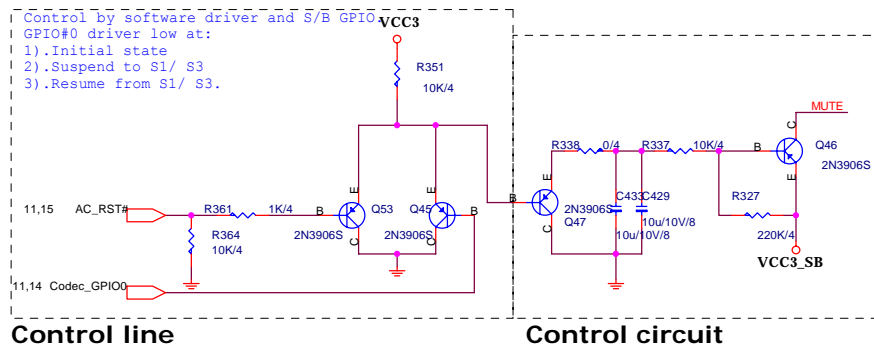


MICRO-STAR INT'L CO., LTD.

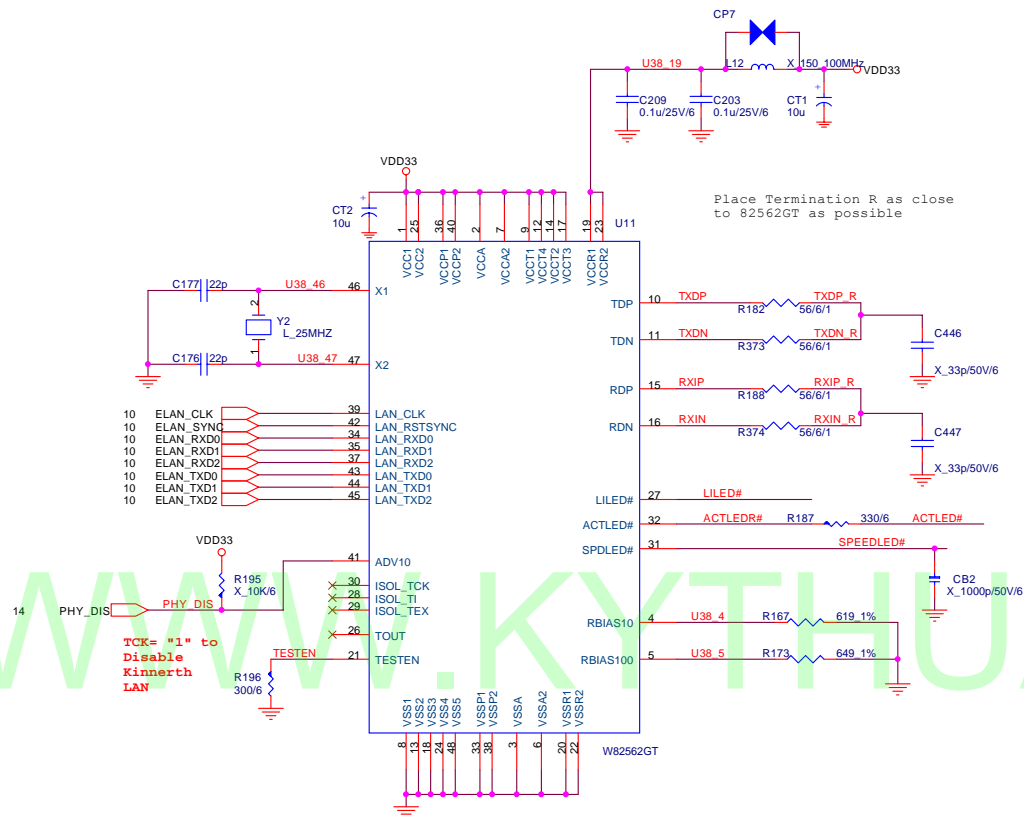
Title			
Azalia & Interanl SPK			
Size	Document Number		Rev
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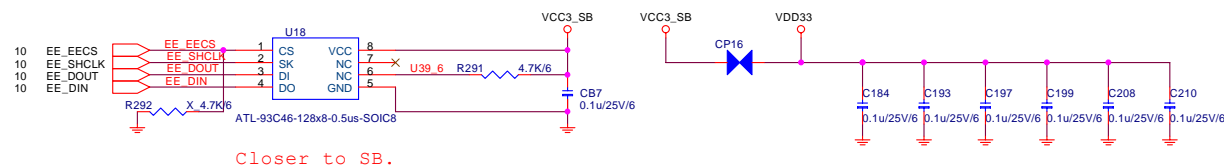
darlington circuit



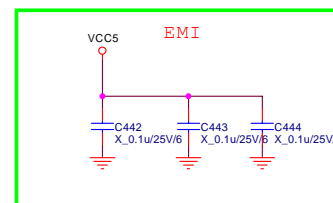
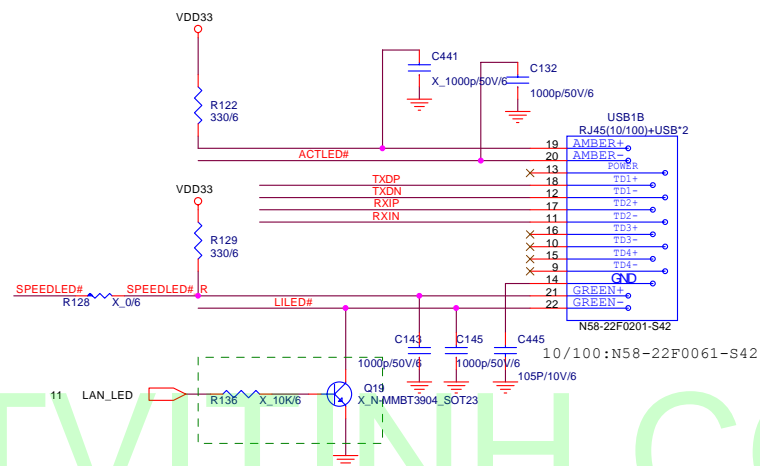
LAN - KINNERITH

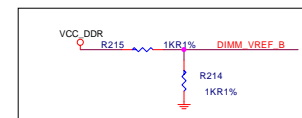
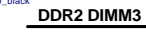


EEPROM



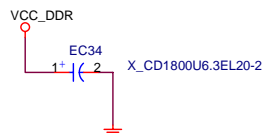
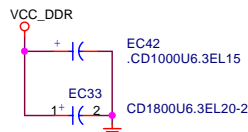
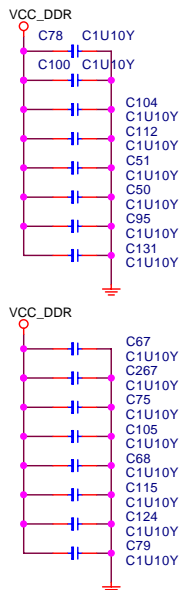
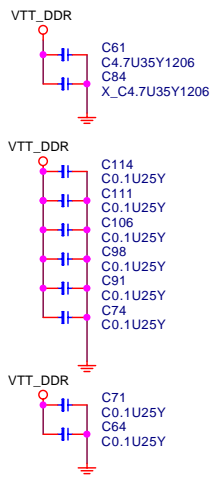
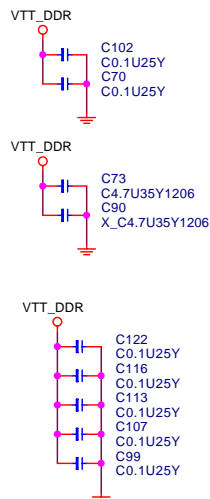
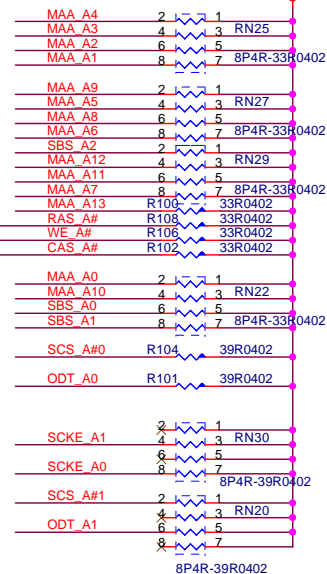
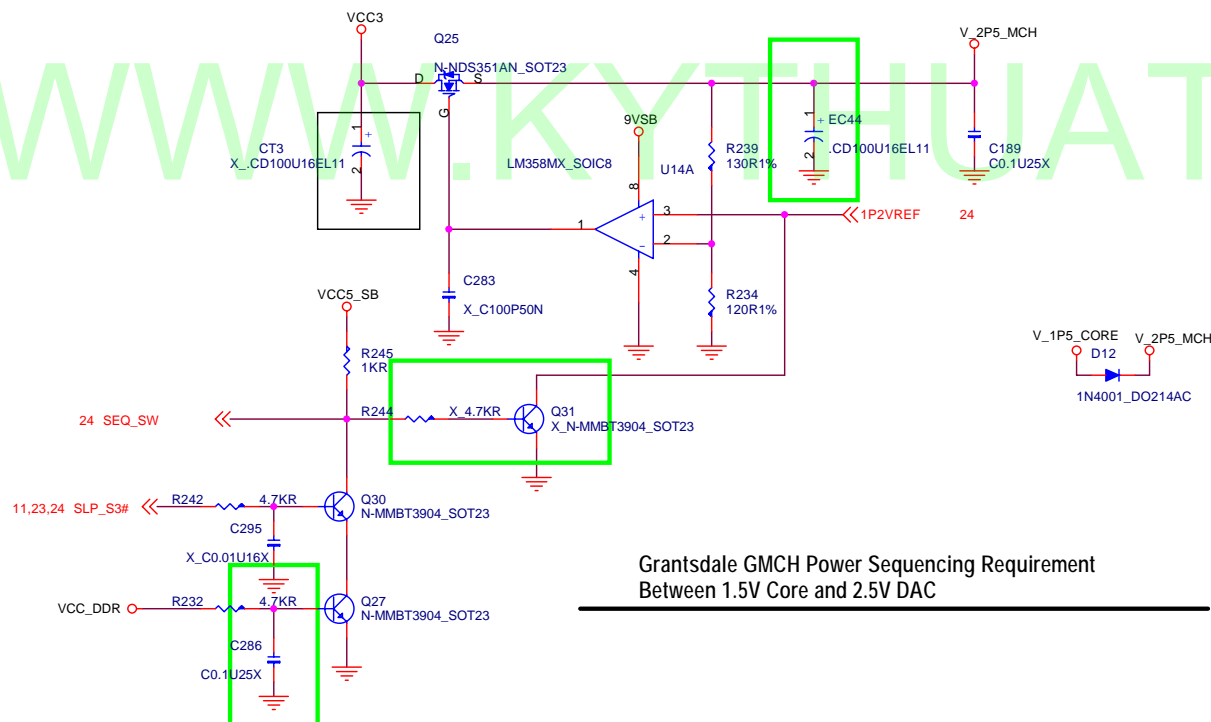
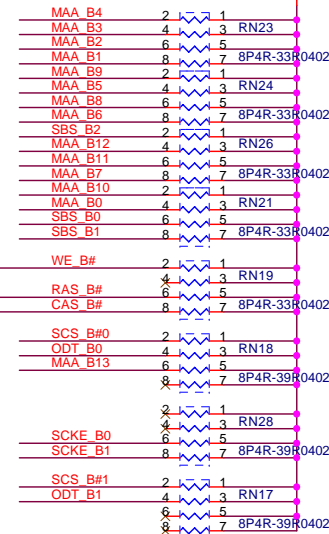
LAN CONNECTOR

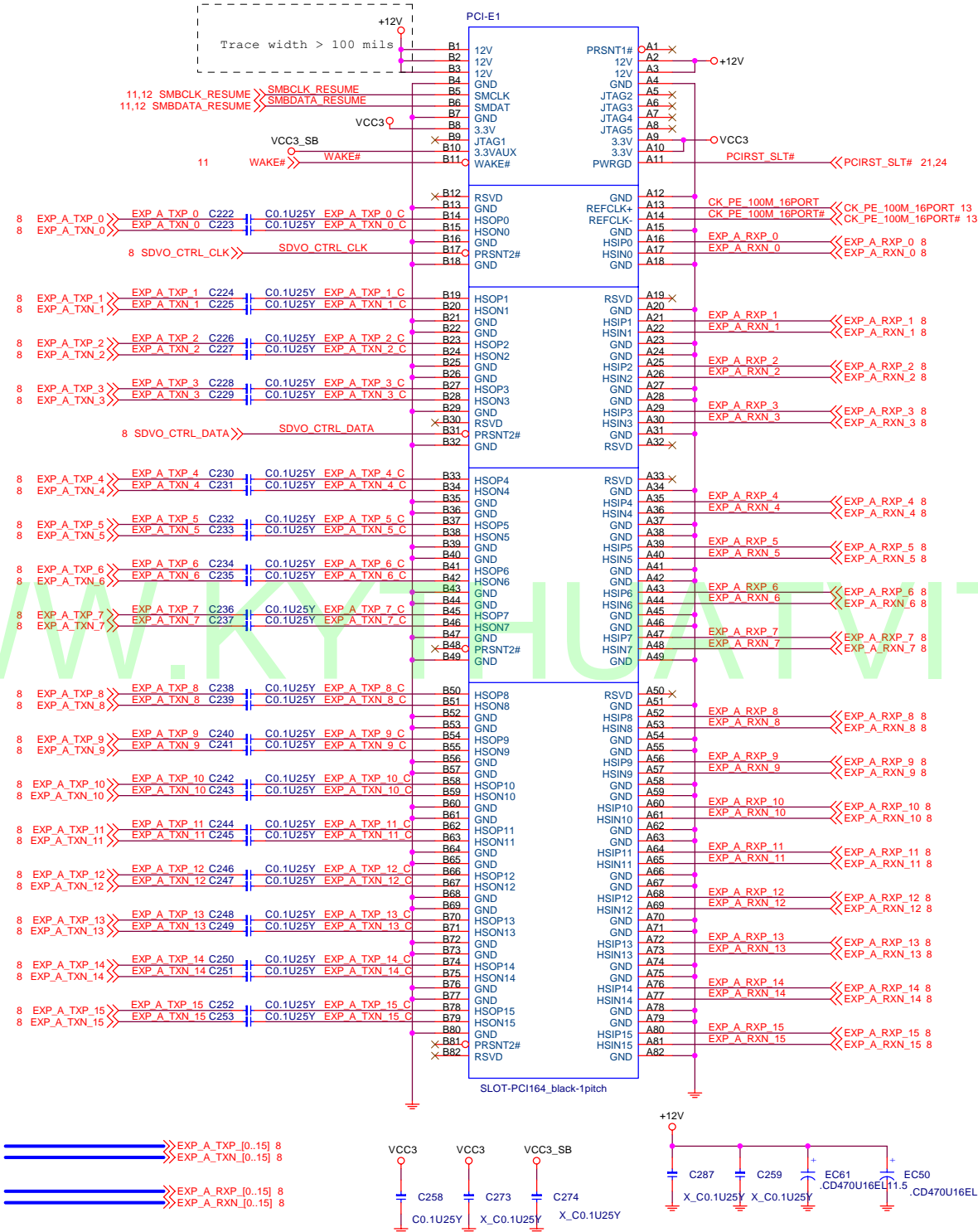




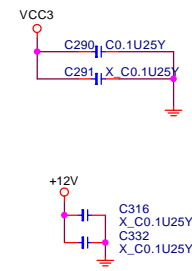
CHANNEL A V_SM_VTT DECOUPLING CAPS

CHANNEL B V_SM_VTT DECOUPLING CAPS

7,18 RAS_A#
7,18 WE_A#
7,18 CAS_A#7,18 WE_B#
7,18 RAS_B#
7,18 CAS_B#7,18 MAA_A[0..13]
7,18 SBS_A[0..2]
7,18 SCS_A#[0..1]
7,18 SCKE_A[0..1]
7,18 ODT_A[0..1]7,18 MAA_B[0..13]
7,18 SBS_B[0..2]
7,18 SCS_B#[0..1]
7,18 SCKE_B[0..1]
7,18 ODT_B[0..1]

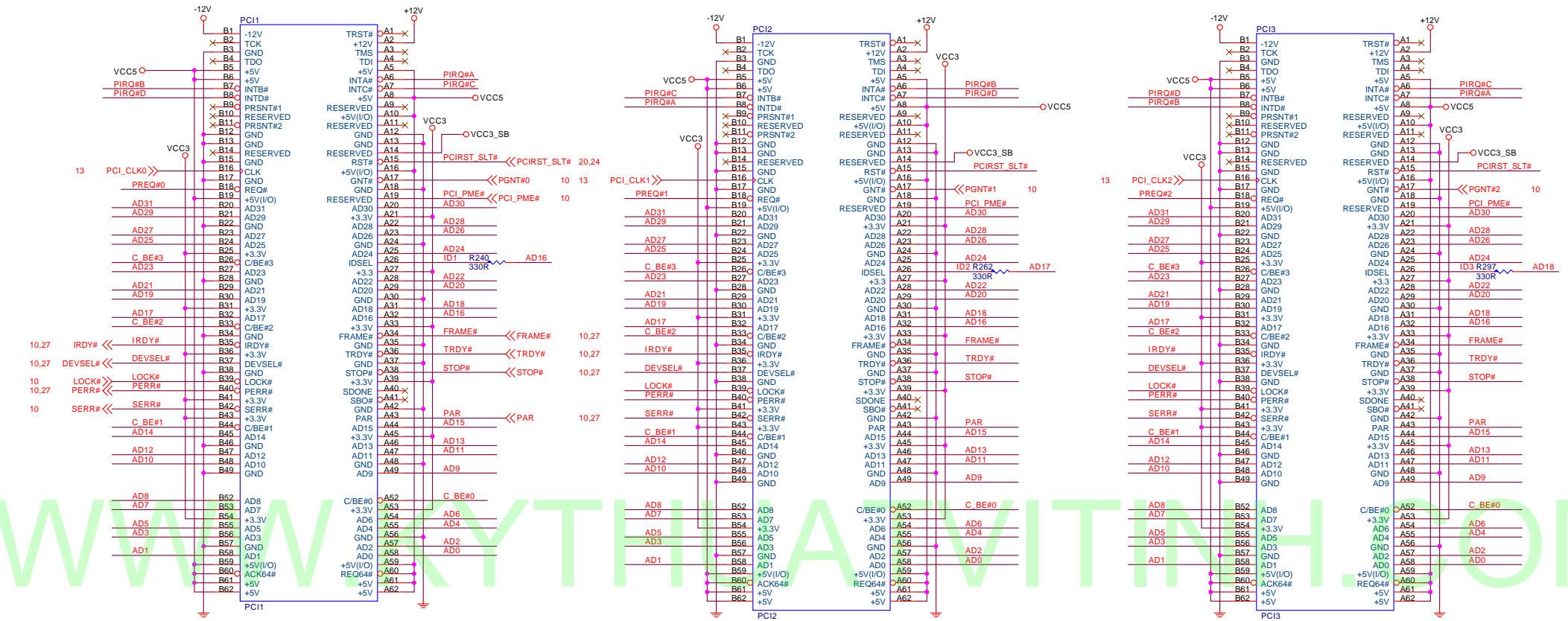


PCI EXPRESS 1-PORT



PCI SLOT 1 (PCI VER: 2.2 COMPLY)

PCI SLOT 2 (PCI VER: 2.2 COMPLY)

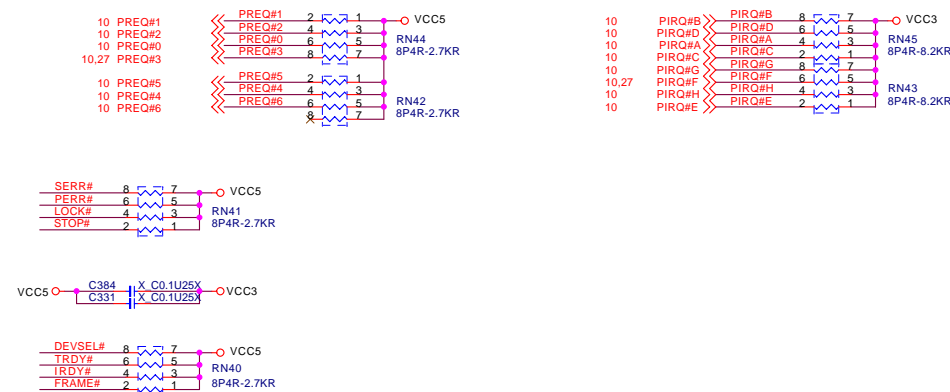


10,27 AD[31..0] << AD[31..0]
IDSEL = AD16
MASTER = PREQ#0
PIRQ#A

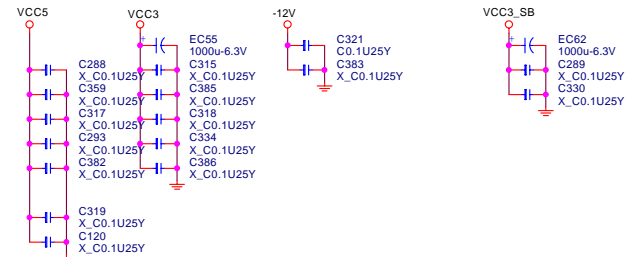
10,27 AD[31..0] << AD[31..0]
IDSEL = AD17
MASTER = PREQ#1
PIRQ#B

10,27 AD[31..0] << AD[31..0]
IDSEL = AD18
MASTER = PREQ#2
PIRQ#C

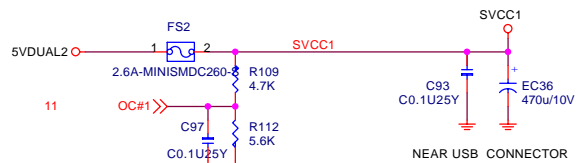
PCI PULL-UP / DOWN RESISTORS



PCI SLOT DECOUPLING CAPACITORS

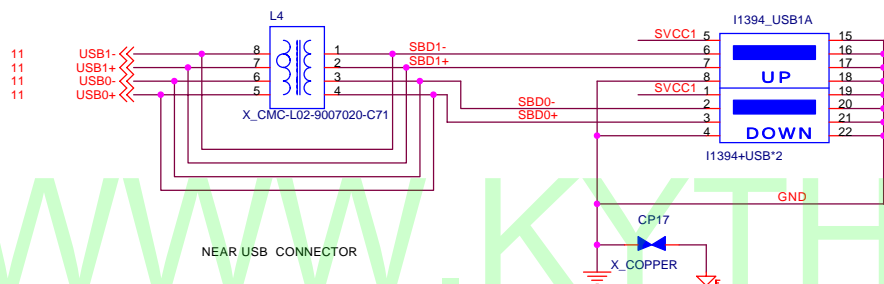


POWER CIRCUIT FOR USB PORT 0,1,2,3 (REAR)

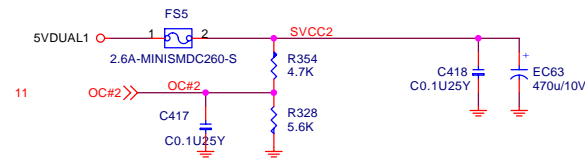


REAR PANEL USB CONNECTOR FOR USB PORT 0,1

USB Interface
Diff. Trace width 7.5 mils & 7.5 mils space.
Diff. & other space 20 mils.
Length matching: < 150 mils
Ttrace length 0" to 17"

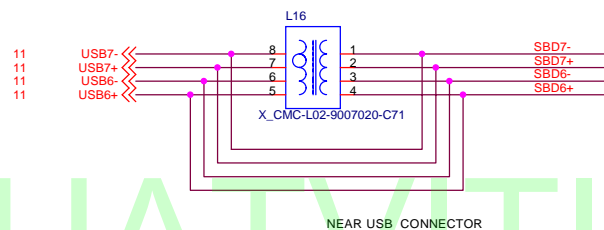


POWER CIRCUIT FOR USB PORT 4,5,6,7 (FRONT)

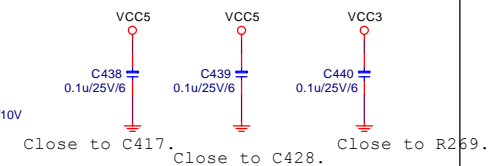


FRONT PANEL USB CONNECTOR FOR USB PORT 6,7

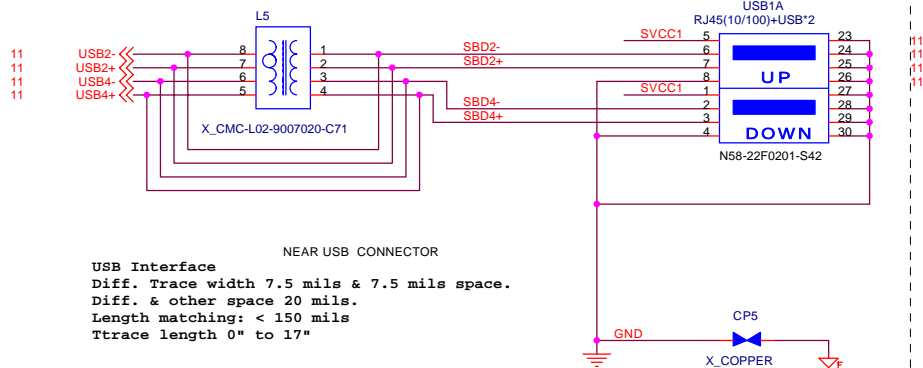
USB Interface
Diff. Trace width 7.5 mils & 7.5 mils space.
Diff. & other space 20 mils.
Length matching: < 150 mils
Ttrace length 0" to 17"



EMI solution

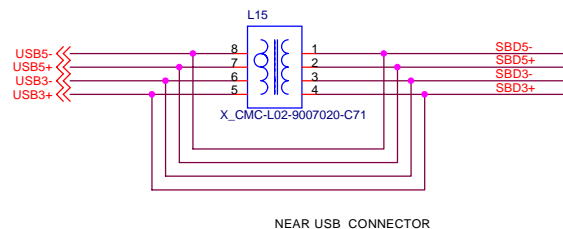


REAR PANEL USB CONNECTOR FOR USB PORT 2,3

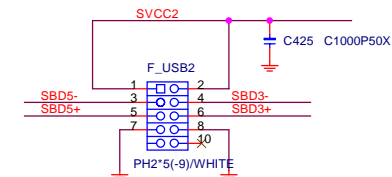


USB Interface
Diff. Trace width 7.5 mils & 7.5 mils space.
Diff. & other space 20 mils.
Length matching: < 150 mils
Ttrace length 0" to 17"

FRONT PANEL USB CONNECTOR FOR USB PORT 4,5



USB Interface
Diff. Trace width 7.5 mils & 7.5 mils space.
Diff. & other space 20 mils.
Length matching: < 150 mils
Ttrace length 0" to 17"



[illegible][illegible]

26 IDEACTP# <<<

11 SATALED# >>>

D16 BAT54A

3

HDDLED#

C406 X_0.1u 25V/6

SERIAL ATA LED

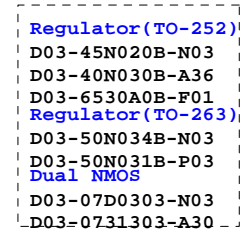
```
G: With 915G option
X: No Stuff
```



Title			
ATX Connector & Front Panel			
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3VSB MODE SELECT	
3VSB MODE	3VDLDEC#
SINGLE MOSFET	PULL HIGH
DUAL MOSFET	PULL LOW

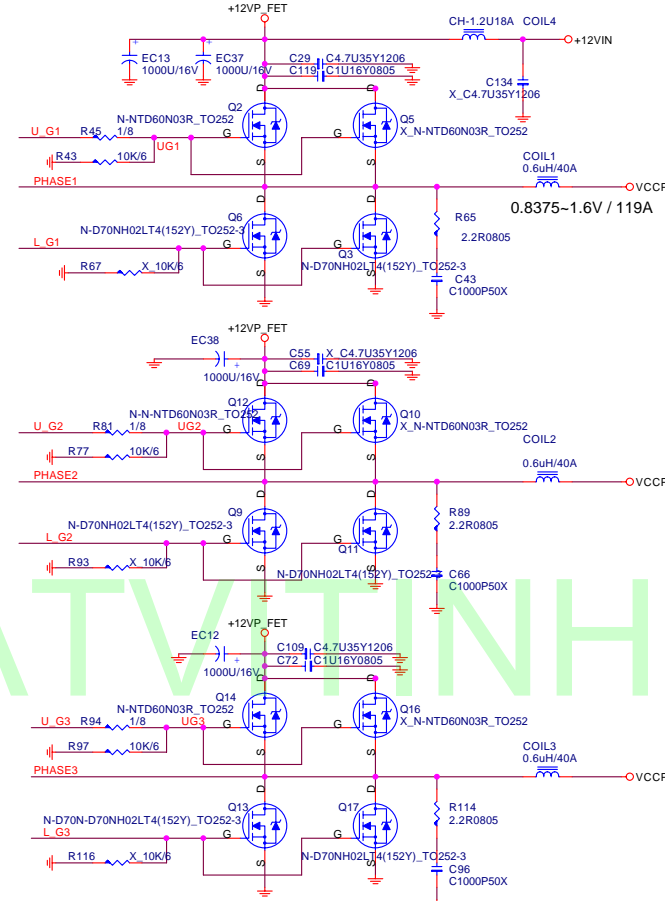
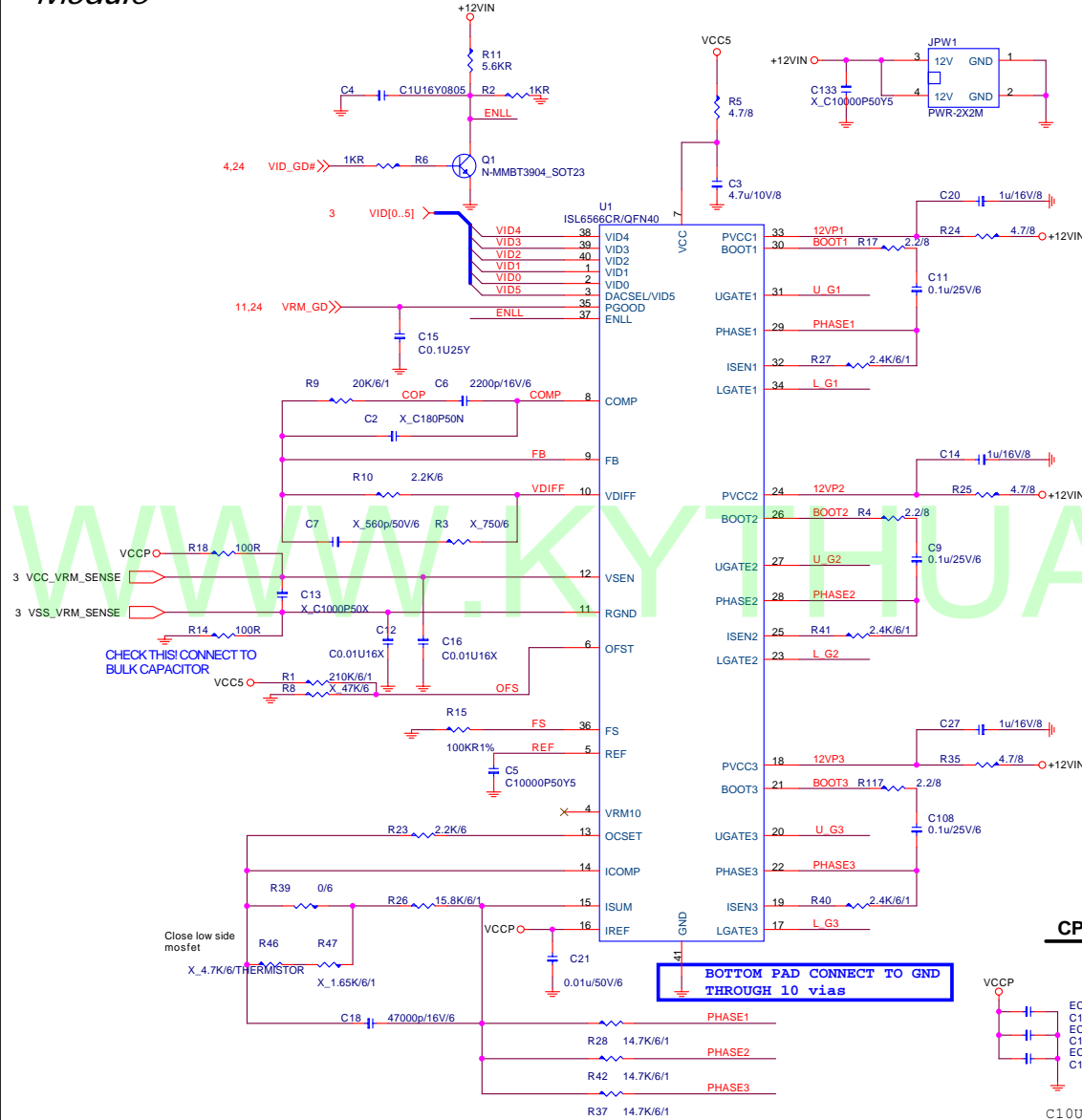
PCI-Express POWER



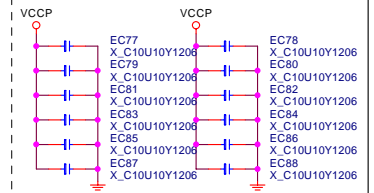
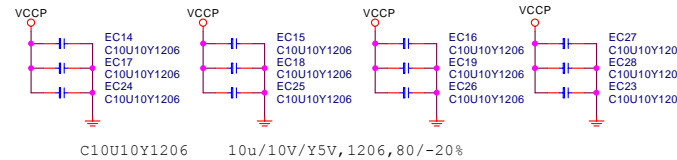
Voltage Regular Module

IPF06N03LA Rds(on)=8.7mΩ (@4.5V, 30A), Vgs(on)=1.2~2V, Id=50A, Ciss=3110pf, Qg=10nC, Vds=25V, Vgs=±20V
C100U2SP ESR<13mΩ, Ripple cur.<2.7A, Lc<12uA, 105C
.CD3300U6.3EL25 ESR<12mΩ, Ripplecur.<2800mA, 105C, longlife3000hrs, KZGSeries
560u_2.5V ESR=6mΩ, Ripplecur.=4400mA, Lc.<500uA, 105C/2000hrs
1800UF/6.3V ESR<12mΩ, Ripplecur.<2350mA, 105C, longlife change from 2000hrs to 3000hrs ,KZJ series
0.6uH/40A
CH-1.2U18A 1.2u/20%, Isat=40A, Rdc=1.2m ohm, PEW wire
1.2u/20%, Dip-2/vertical 17.5mm, 1.2ψ/5.5turns, 18A

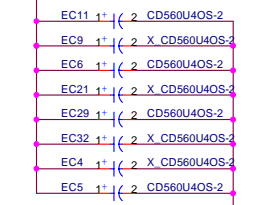
TDP = 115 W
VR_TDC = 101 A
Icc(max) = 119 A
Tejas Tcase = [P x 0.213] + 43.3
Prescott Tcase = [P x 0.25] + 43.3



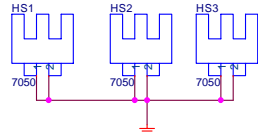
CPU DECOUPLING CAPACITORS



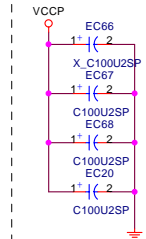
OS-CON Capacitors



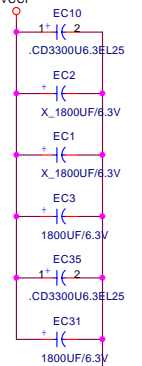
MOSFET Heatsinks



SP Capacitors



EL Capacitors



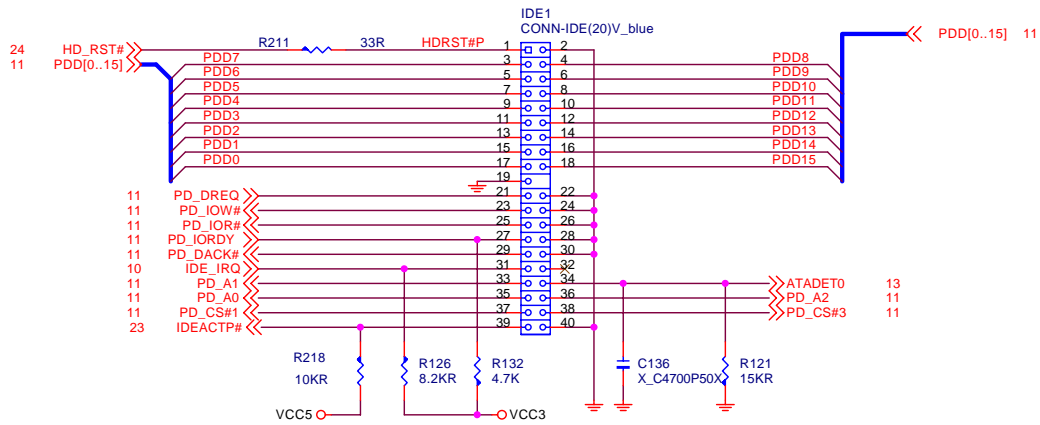
Solder Side



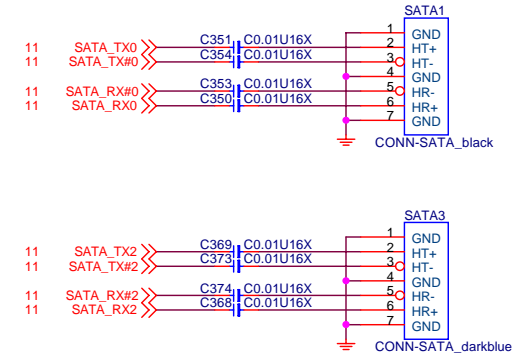
MICRO-STAR IN'L CO., LTD.

Title			VRM 10.1 - Intersil 6565ACV 3 Phase
Size	Document Number	MS-7174H1	
Date	Thursday, June 30, 2005	Sheet	25 of 31

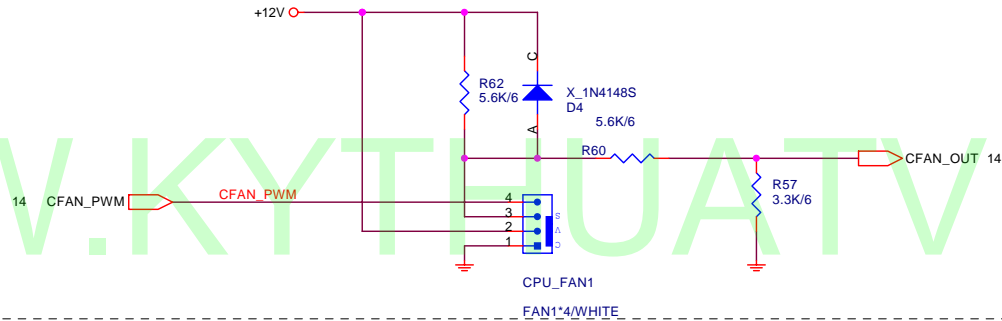
ATA 33/66/100 IDE Connectors



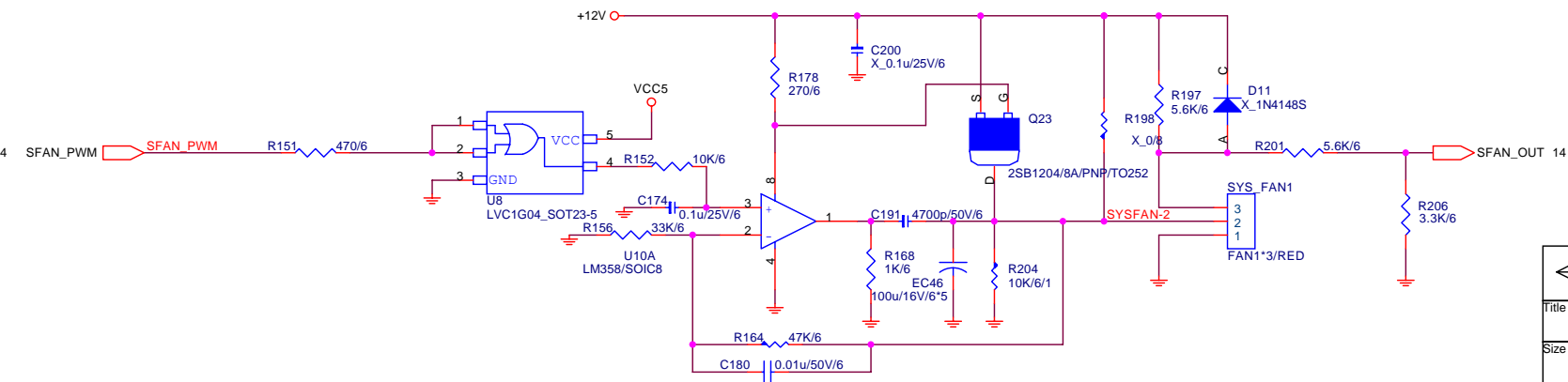
SERIAL ATA CONNECTOR BLOCK



CPU FAN



SYSTEM FAN



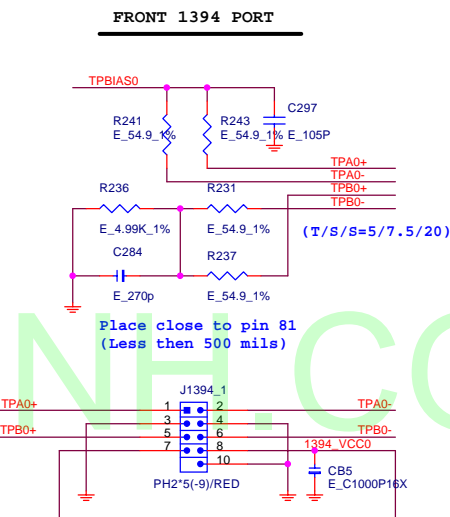
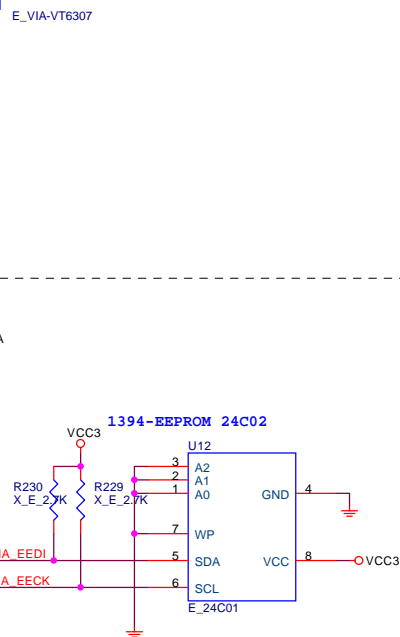
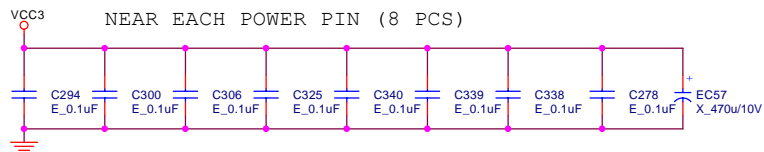
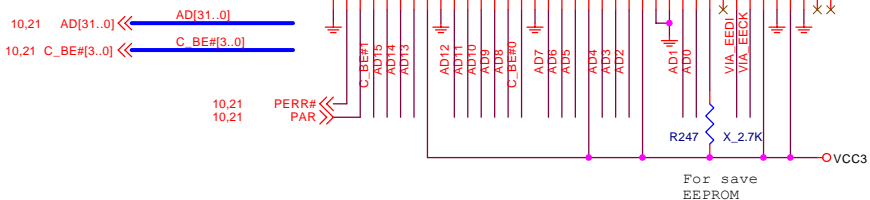
MSI MICRO-STAR INT'L CO., LTD.			
Title FAN & IDE Connectors			
Size	Document Number	MS-7174H1	Rev 1B
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X: No Stuff

```


IDSEL = AD25
MASTER = PREQ#3
PIRQ#F

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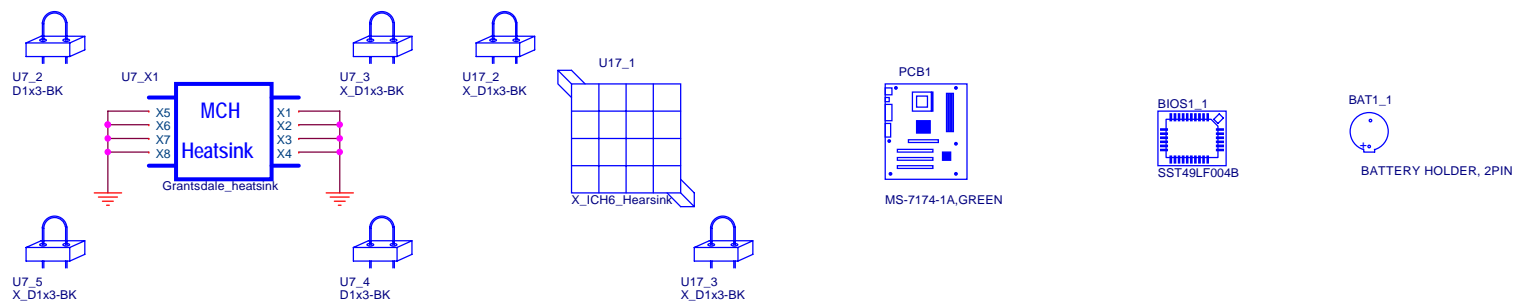


(T/S/S=5/7.5/20)

Place close to pin 74
(Less then 500 mils)

 MICRO-START INT'L CO., LTD.			
Title			
VIA-6307 IEEE1394 Controller			
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A3	MS-7174H1	1B	
Date:	Thursday, June 30, 2005	Sheet	27 of 31

MANUAL PART



910GEC0
Cfg7174-910GE = _
Cfg7174-915GV = X
Opt Part = Y



ICH6C0
Cfg7174-910GE = _
Cfg7174-915GV = X
Opt Part = Y



915GV
Cfg7174-910GE = X
Cfg7174-915GV = _
Opt Part = Y



ICH6B2
Cfg7174-910GE = X
Cfg7174-915GV = _
Opt Part = Y

Model option table

Model type	Function	BOM Config	ERP BOM No.
MS7174 (Graphite)	915GV+ ICH6+ 47M997+ 82562GT+ ALC880+ 3PCI+ u-ATX + 2PS2+ 8USB+ 1COM+ VGA+ 1Audio+ LPT+ RJ45+ Intel pinhead+ 1394	Cfg7174-915GV	601-7174-***
MS7174 (Gypsum)	910GE+ ICH6+ 47M997+ 82562GT+ ALC880+ 3PCI+ PCIE X16+ u-ATX+ 2PS2+ 8USB+ 1COM+ VGA+ 1Audio+ LPT+ RJ45+ Intel pinhead+ 1394	Cfg7174-910GE	601-7174-02S



MICRO-STAR INT'L CO., LTD.

Title		General Purpose Spec & JUMPER SETTING	
Size	Document Number	MS-7174H1	Rev 1B
Date:	Thursday, June 30, 2005	Sheet	28 of 31